

Notice of References Cited		Application/Control No.	Applicant(s)/Patent Under R examination YOO, WOO SIK	
		Examiner Stanetta D. Isaac	Art Unit 2812	Page 1 of 1

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NON-PATENT DOCUMENTS

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*	U	Stanley Wolf and Richard N.Tauber, Silicon Processing For The VLSI Era, 2000, Lattice Press, Second Edition, pages 259-261
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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